IN THE UNITED STATES PATENT AND TRADEMARK OFFICE.

Applicant : Shunpei Yamazaki, et al. Art Unit : 1792

Serial No.: 09/892,225 Examiner: Mathew J. Song

Filed: June 25, 2001 Confirmation No.: 1969

Notice of Allowance Date: November 4, 2008
Title : SEMICONDUCTOR DEVICE AND FABRICATION METHOD THEREFOR

MAIL STOP ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed November 4, 2008, enclosed is a completed issue fee transmittal form PTOL-85b. The amount of \$1,810 for the required issue and publication fees is being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

Date: February 3, 2009

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